

REMARKS

The application has been amended and is believed to be in condition for allowance.

Previously pending claims 1-8 have been replaced with new claims 9-14. These claims are based on the previously pending claims as discussed below.

The Official Action objected to the previously filed declaration. Responsively, a replacement Declaration and Power of Attorney is enclosed.

The Official Action objected to certain formal matters with the originally filed claims. The newly presented claims have been drafted so as to avoid these objections.

The Official Action rejected claim 1 under §112, second paragraph, as being indefinite. New claim 9 has been drafted so as to remedy the stated basis of rejection.

Original claims 1-8 were rejected as anticipated by FRANCISCO et al. 5,263,147.

Newly presented claim 9 is believed patentable over this reference as discussed below.

According to the present invention, there is a computer provided with a processor and there is a security device provided with a processor. Further, there are a number of switches at certain positions such as connecting points to different resources, which switches are controlled by the security device. By means of these switches, the security device has full control

over the computer when a security critical activity shall be done.

At the same time, the switches allow the processor of the computer to have access to resources that the security device does not use.

The control over the resources by the switches makes a configuration possible, where certain memory areas are allocated to the security device **only**.

When a security critical activity shall be done, the activity is done by means of the security device and with the resources which are only accessible for the processor of the security device according to the information in the switches.

When a security critical activity is ordered, a signal is sent by the security device from a signal generator SG to a protection mode signal receiver SR of the switch. This method controls the access of the processor of the computer, as well as the access of the processor of the security device to different resources.

According to another feature of the invention, the signals SG can only be generated by the security device.

Further, yet another feature is that the security device contains a signal generator, which when a switch receives a signal, together with new information, is capable of changing the contents of the information in that switch.

These features were stated in original claims 2 and 4 and have now been introduced into claim 9.

Thus, the present invention refers to resources for the security device in such a way that a separate security device is contained in an existing computer, such as a PC. Thus, resources are designated for use by the security device.

In contrast, FRANCISCO et al. disclose a system where a security critical activity is processed in the processor of the computer. The security processor only has the function to check if all activities taken by the main processor are allowed, when the main processor executes the security critical activity. If an activity is not allowed, it will be stopped. Thus, only a security policy is introduced into the security device.

Thus, FRANCISCO et al. do not disclose the present invention as expressed in new independent claim 9.

Referring to the Official Action, pages 4-5, on top of page 5, it is stated that the signal received from the security device denies the computer processor access to resources selected by the security device. This is wrong. In FRANCISCO et al. a security critical activity, which is allowed, is processed by the computer processor which during execution has access to selected resources necessary for the security critical activity. In such state the computer processor is not denied access to any resource by the security device.

However, if the computer processor processes a security critical activity, which is not allowed according to the policy, the security device will deny access for the computer processor to resources that would be necessary for the activity.

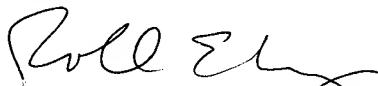
Again, according to the present invention, a security critical activity is executed by the processor of the security device, while the processor of the computer is denied access to the resources used by the security device for executing the security critical activity.

For the reasons outlined above, the applied reference is not believed to be anticipatory and the claims are believed allowable. Accordingly, reconsideration and allowance of all the pending claims are respectfully requested.

The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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APPENDIX:

The Appendix includes the following item:

- Combined Declaration and Power of Attorney